patent application, which accompanied Applicants' February 12, 2004 Supplemental Notice of Copying Claims, copied these amendments.

In the current office action, the Examiner is requesting that the Applicants specifically apply each limitation or element of each of the copied claims to the disclosure of the above captioned patent application pursuant to 37 C.F.R. § 1.607(a)(5) and MPEP § 2307. Applicants note that as the above captioned application and the application with which Applicants are trying to interfere are both pending, 37 C.F.R. § 1.607 is inapplicable and the applicable rule is 37 C.F.R. § 1.604 which does not have a similar requirement to 37 C.F.R. § 1.607(a)(5).

However, in the interests of expediting Applicants' request for an interference, with this response, claim charts have been provided below which identify support for each limitation or element of the copied claims. No new matter has been added.

The Examiner is also encouraged to review the file history of the '262 application as the parent patent application to the above captioned patent application was used as a cited reference in an office action in that case. In the '262 application, the Examiner cited relevant portions of the parent application, the subject matter of which was incorporated into the above captioned patent application.

Accordingly, reconsideration of both the application and Applicants' request for interference is respectfully requested in light of the following claim charts.

Claim	Citation to U.S. Patent Application Serial No. 10/712,960
50. An electric power meter, comprising:	Para. 39, lines 2; para. 90, line 12; and para. 122, line 2.
a digital sampler for digitally sampling voltage and current;	Para. 122, line 7; para. 132, line 3; para. 133; and para 188.
a memory for storing said digitally sampled voltage and current;	Para. 39, line 7; paras. 43, 44, 67, 69; para 122; and Fig. 15.
at least one processor for performing	Para. 42; para. 122; para. 6, lines 3-5; Figs.

power calculations on said digitally sampled voltage and current, and converting said calculations and said digitally sampled voltage and current into at least one network protocol,

1 and 15; para. 39, line 7; para. 41, line 5; para. 42; para. 43; para. 122; para. 136; and paras. 42, 41, 5.

said at least one processor being configured to simultaneously execute a plurality of different tasks related to said stored voltage and current in response to a plurality of concurrent requests related to results of said different tasks and submitted by multiple users; and Para. 62, lines 8-11; para. 161, lines 8-11; para. 50; para. 60; para. 65, lines 6-7; para. 99; para. 164, lines 6-7; para. 191, lines 2-3; para. 62; para. 65; para. 67, line 9; and para. 39

a network interface for interfacing with an external network;

Para. 59, lines 2-4; para. 158, lines 9-11; para. 39, line 7; para. 60; and paras. 38 and 41.

wherein said network protocol is one of e-mail, File Transfer Protocol (FTP), Hypertext Transfer Protocol (HTTP) or Dynamic Host Configuration Protocol (DHCP).

tasks related to said stored voltage and current

in response to a plurality of concurrent

Para. 166, lines 6-12; para. 59, lines 7-10; para. 67, lines 6-12; para. 159, lines 5-9; para. 179, line 2; para. 98, line 2; para. 38, lines 6-10; and paras. 38 and 59.

99; para. 164, lines 6-7; para. 191, lines 2-

3; para. 62; para. 65; para. 67, line 9; and

Claim Citation to U.S. Patent Application Serial No. 10/712,960 Para. 39, lines 2; para. 90, line 12; and para. 51. An electric power meter, comprising: 122, line 2. Para. 122, line 7; para. 132, line 3; para. a digital sampler for digitally sampling voltage and current; 133; and para 188. a memory for storing said digitally Para. 39, line 7; paras. 43, 44, 67, 69; para sampled voltage and current; 122; and Fig. 15. at least one processor for performing Para. 42; para. 122; para. 6, lines 3-5; Figs. power calculations on said digitally sampled 1 and 15; para. 39, line 7; para. 41, line 5; voltage and current, and converting said para. 42; para. 43; para. 122; para. 136; and calculations and said digitally sampled voltage paras. 42, 41, 5. and current into at least one network protocol, Para. 62, lines 8-11; para. 161, lines 8-11; said at least one processor being configured to simultaneously execute a plurality of different para. 50; para. 60; para. 65, lines 6-7; para.

requests related to results of said different tasks and submitted by multiple users; and

a network interface for interfacing with an external network;

Para. 59, lines 2-4; para. 158, lines 9-11; para. 39, line 7; para. 60; and paras. 38 and 41.

wherein a web server provides data to the network interface in Hypertext Markup Language (HTML) or Extensible Markup Language (XML) format.

Claim

52. An electric power meter having a digital sampler for sampling a voltage and a current at a sampling point, comprising:

a processor coupled to said digital sampler and configured to execute a plurality of different tasks related to said sampled voltage and current and running independently from one another in response to a plurality of concurrent requests submitted by multiple users;

a memory coupled to said processor for storing network protocol conversion algorithms; and

a network interface configured to simultaneously provide said multiple users each with a result of a respective one of said plurality of different tasks;

wherein said processor performs at least one power calculation and converts at least one of the sampled voltage, the sampled current and the power calculation to at least one network protocol using one of said network protocol conversion algorithms, said at least one network protocol being provided through

Citation to U.S. Patent Application Serial No. 10/712,960

Para. 39, line 2; para. 90, line 12; para. 122, line 2; para. 122, line 7; para. 132, line 3; para. 133; and para. 188.

Para. 42; para. 122; para. 6, lines 3-5; Figs. 1 and 15; para. 39, line 7; para. 41, line 5; para. 42; para. 43; para. 122; para. 136; para. 62, lines 8-11; para. 161, lines 8-11; para. 50; para. 60; para. 65, lines 6-7; para. 99; para. 164, lines 6-7; para. 191, lines 2-3; para. 62; para. 67, line 9; para. 68; and paras 42, 41, 5, 17 and 39.

Para. 39, line 7; paras. 43, 44, 67, 69; para. 122; and Fig. 15.

Para. 59, lines 2-4; para. 158, lines 9-11; para. 39, line 7; para. 60; and paras. 38 and 41.

see above for processor; paras. 42, 41, 8, 38, and 41.

said network interface;

wherein said network protocol is one of e-mail, File Transfer Protocol (FTP), Hypertext Transfer Protocol (HTTP) or Dynamic Host Configuration Protocol (DHCP). Para. 166, lines 6-12; para. 59, lines 7-10; para. 67, lines 6-12; para. 159, lines 5-9; para. 179 line 2; para. 98, line 2; and para. 38, lines 6-10.

Claim

53. An electric power meter having a digital sampler for sampling a voltage and a current at a sampling point, comprising:

a processor coupled to said digital sampler and configured to execute a plurality of different tasks related to said sampled voltage and current and running independently from one another in response to a plurality of concurrent requests submitted by multiple users;

a memory coupled to said processor for storing network protocol conversion algorithms; and

a network interface configured to simultaneously provide said multiple users each with a result of a respective one of said plurality of different tasks;

wherein said processor performs at least one power calculation and converts at least one of the sampled voltage, the sampled current and the power calculation to at least one network protocol using one of said network protocol conversion algorithms, said at least one network protocol being provided through said network interface;

wherein a web server provides data to the network interface in Hypertext Markup

Citation to U.S. Patent Application Serial No. 10/712,960

Para. 39, line 2; para. 90, line 12; para. 122, line 2; para. 122, line 7; para. 132, line 3; para. 133; and para. 188.

Para. 42; para. 122; para. 6, lines 3-5; Figs. 1 and 15; para. 39, line 7; para. 41, line 5; para. 42; para. 43; para. 122; para. 136; para. 62, lines 8-11; para. 161, lines 8-11; para. 50; para. 60; para. 65, lines 6-7; para. 99; para. 164, lines 6-7; para. 191, lines 2-3; para. 62; para. 67, line 9; para. 68; and paras 42, 41, 5, 17 and 39.

Para. 39, line 7; paras. 43, 44, 67, 69; para. 122; and Fig. 15.

Para. 59, lines 2-4; para. 158, lines 9-11; para. 39, line 7; para. 60; and paras. 38 and 41.

see above for processor; paras. 42, 41, 8, 38, and 41.

Para. 166, lines 6-12; para. 59, lines 7-10; para. 67, lines 6-12; para. 159, lines 5-9;

Language (HTML) or Extensible Markup	para. 179 line 2; para. 98, line 2; and para.
Language (XML) format.	38, lines 6-10.

Accordingly, pursuant to 37 C.F.R. § 1.604(a)(3), an interference should be declared because the claims satisfy the test of two-way unpatentability in accordance with 37 C.F.R. § 1.601(n), i.e. the above claims 50 and 51 of the above captioned application would be unpatentable over corresponding claims 10 and 11, as amended, of U.S. Patent Application Serial No. 10/121,262 if the inventors thereof are determined to be the prior inventor and claims 10 and 11, as amended, of U.S. Patent Application Serial No. 10/121,262 would be unpatentable over the above claims 50 and 51 of the above captioned application otherwise; and further the above claims 52 and 53 of the above captioned application would be unpatentable over corresponding claims 4 and 5, as amended, of U.S. Patent Application Serial No. 10/121,262 if the inventors thereof are determined to be the prior inventor and claims 4 and 5, as amended, of U.S. Patent Application Serial No. 10/121,262 would be unpatentable over the above claims 52 and 53 of the above captioned application otherwise.

The Examiner is invited to call the undersigned if it would expedite the prosecution of this application.

Respectfully submitted,

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